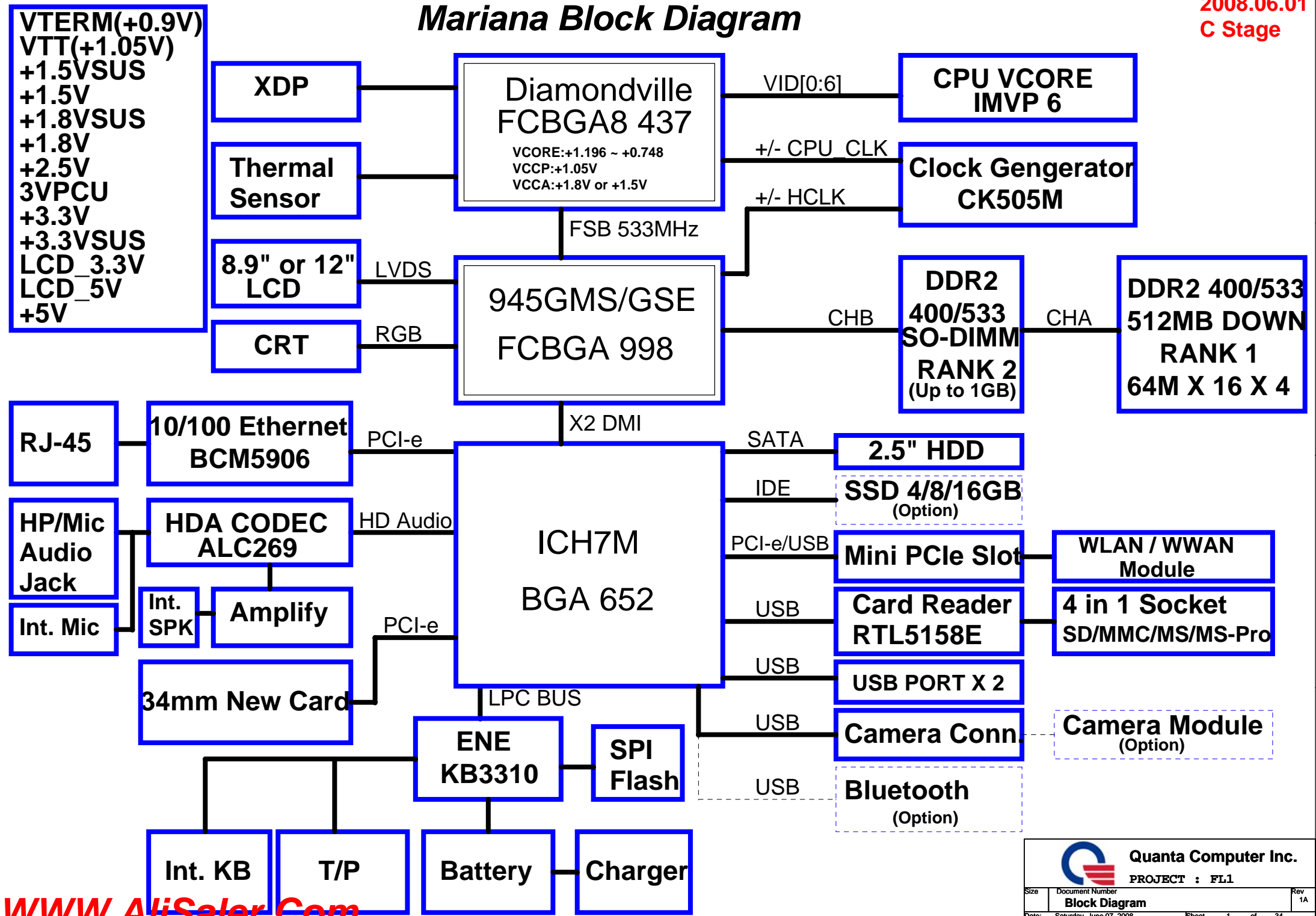
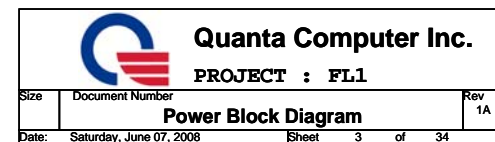
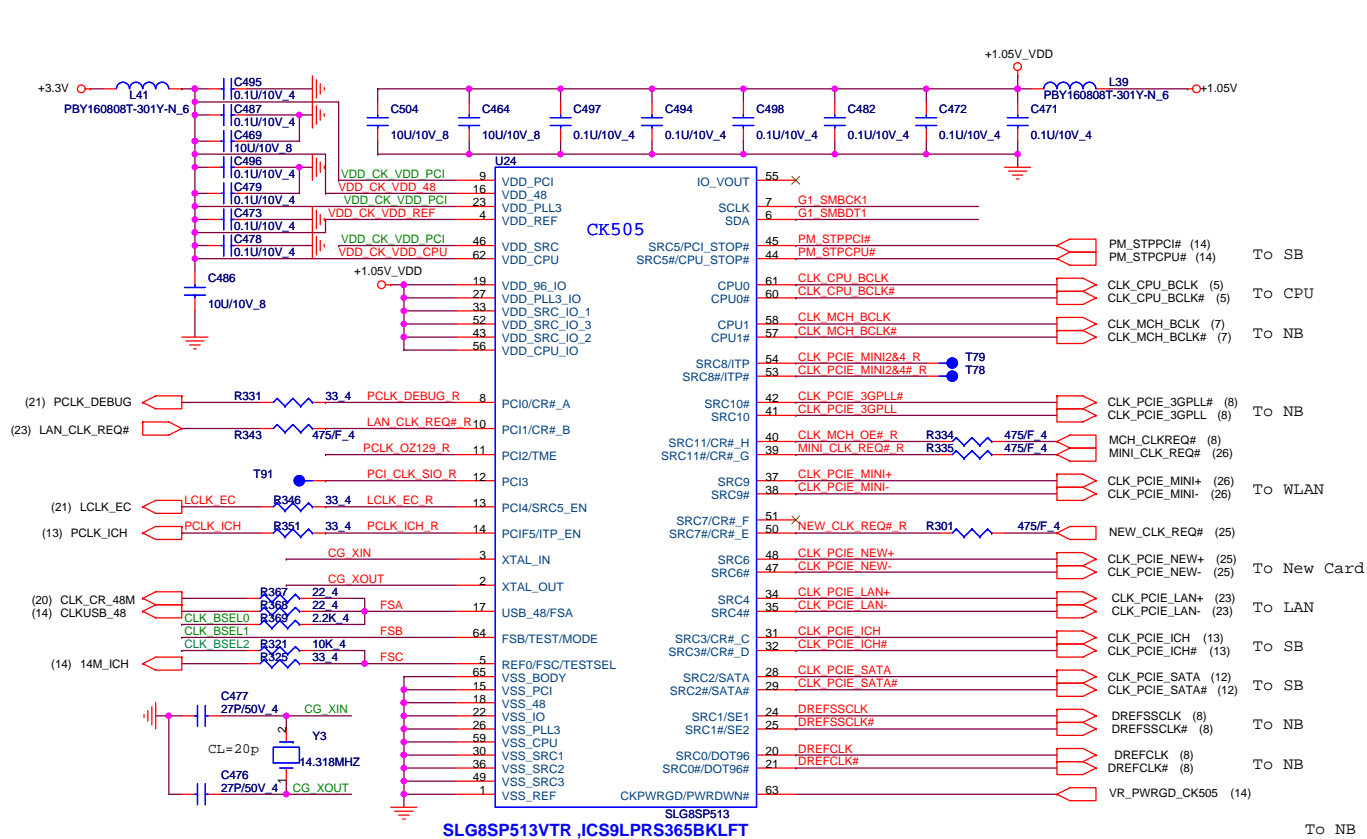


Mariana Block Diagram



[illegible][illegible][illegible]

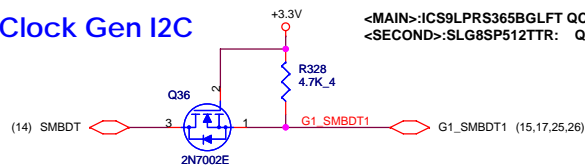




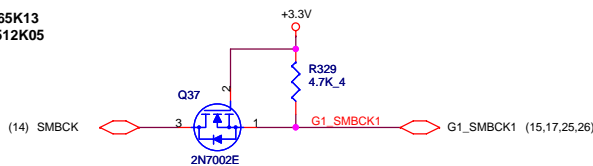
(21) PCLK_DEBUG
(23) LAN_CLK_REQ#
(21) LCLK_EC
(13) PCLK_ICH
(20) CLK_CR_48M
(14) CLKUSB_48
(14) 14M_ICH

	ICS9LPRS365 (ALPRS365K13)	SLG8SP513 (AL000875K06)	PULL HIGH	PULL DOWN
Pin 11	PCI2/TME	PCI2/TME	NO OVERCLOCKING (default)	NORMAL RUN
Pin 12	PCI-3/SRC5_EN	PCI-3/SRC5_EN	PIN37/38 IS SRC5	PIN37/38 IS SRC5
Pin 13	PCI-4/27M_SEL	PCI-4/27M_SEL	PIN 17/18 IS 27MHz	PIN 17/18 IS SRC/DOT
Pin 14	PCIF-5/ITP_EN	PCIF-5/ITP_EN	PIN 46/47 IS CPUITP	PIN 46/47 IS SRC8 (default)

Clock Gen I2C

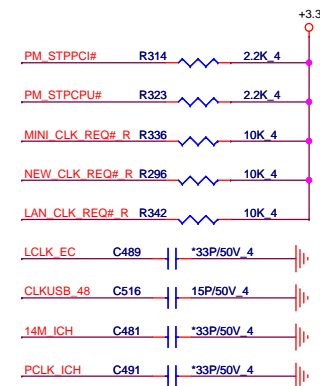


<MAIN>: ICS9LPRS365BGLFT QCI: ALPRS365K13
<SECOND>: SLG8SP512TTR: QCI: AL8SP512K05



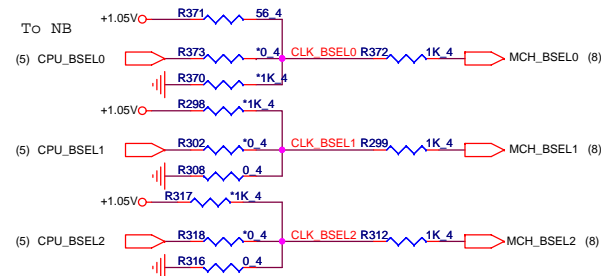
SM_BUS_Group1

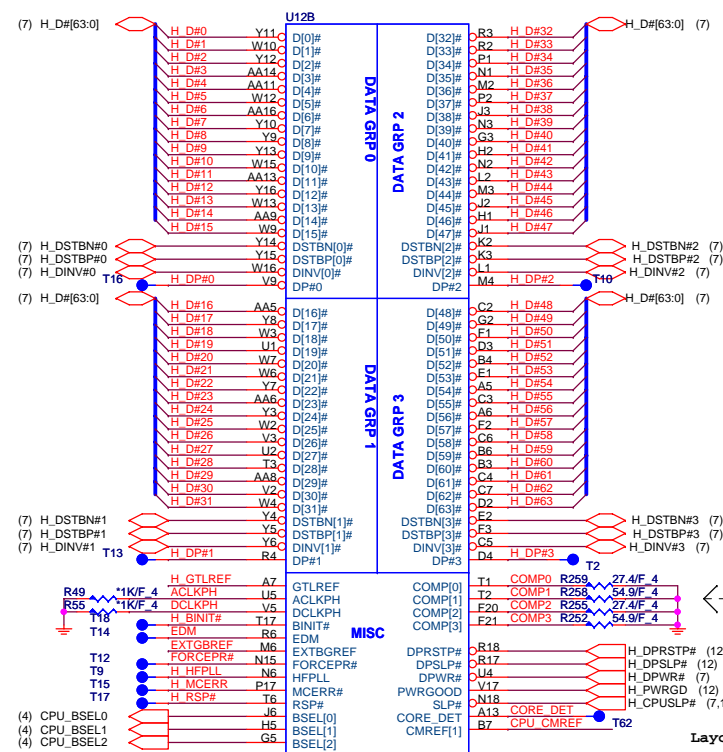
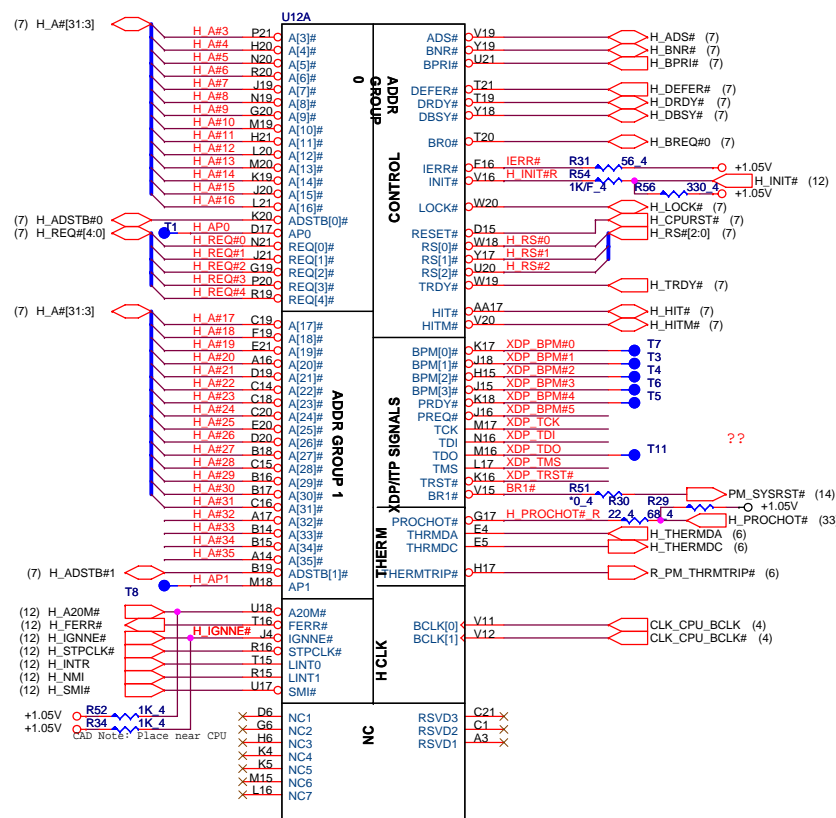
1. Clock Gen
2. Memory Rom
3. Mini Card
4. New Card
5. CPU Thermal Monitor



Frequency select

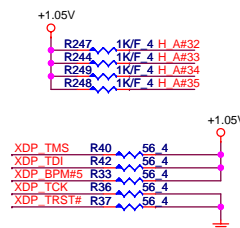
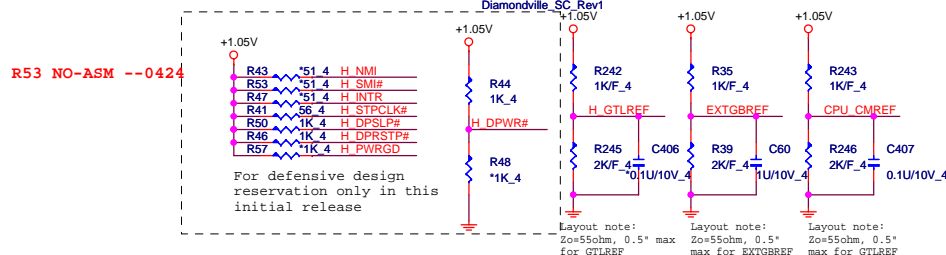
FSC	FSB	FSA	CPU	SRC	PCI
1	0	1	100	100	33
0	0	1	166	100	33
0	1	0	200	100	33
0	0	0	266	100	33
1	0	0	333	100	33
1	1	0	400	100	33
1	1	1	Reserved		

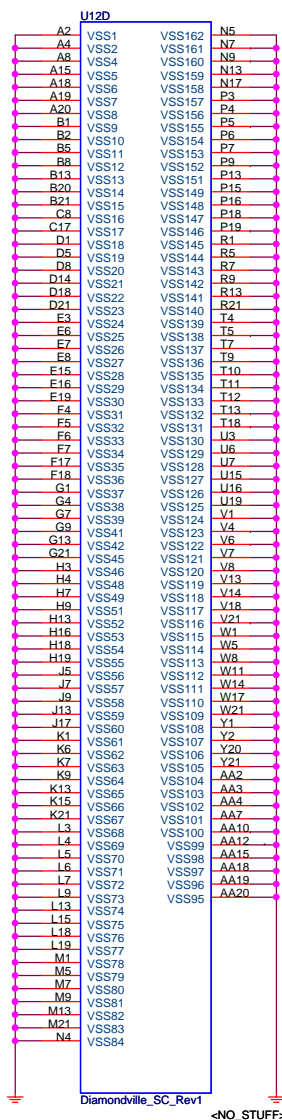


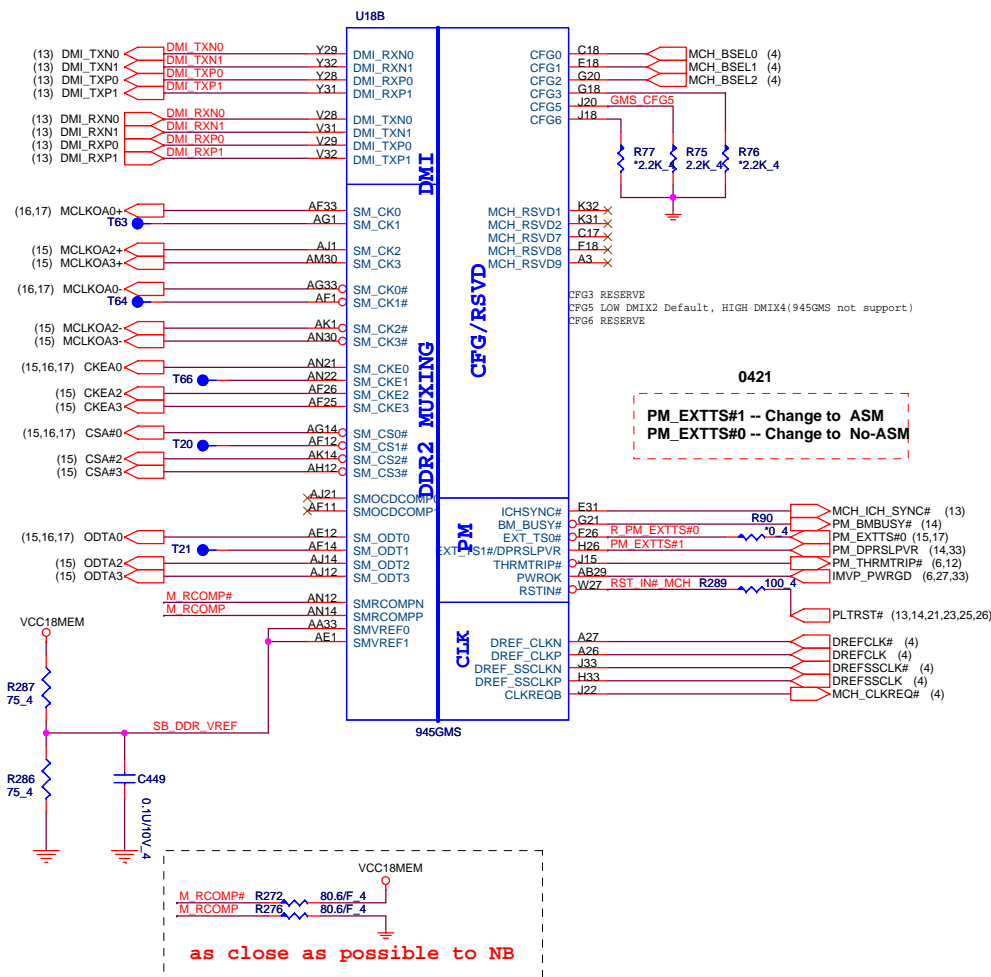


Layout note:

Comp0,2 connect with Zo=27.4ohm, make trace length shorter than 0.5"
Comp1,3 connect with Zo=55ohm, make trace length shorter than 0.5"





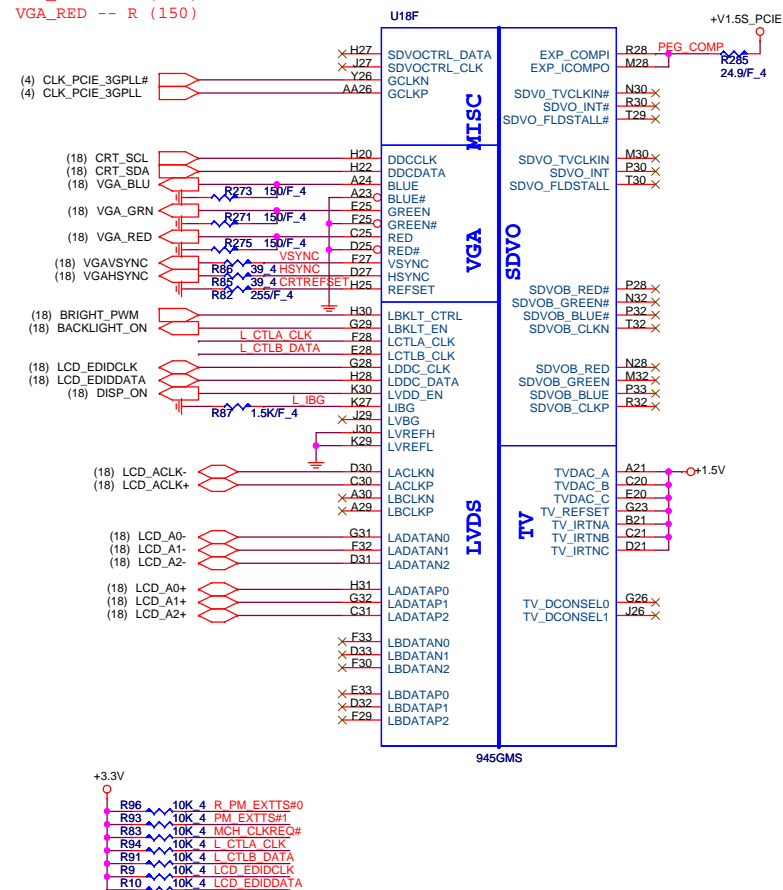


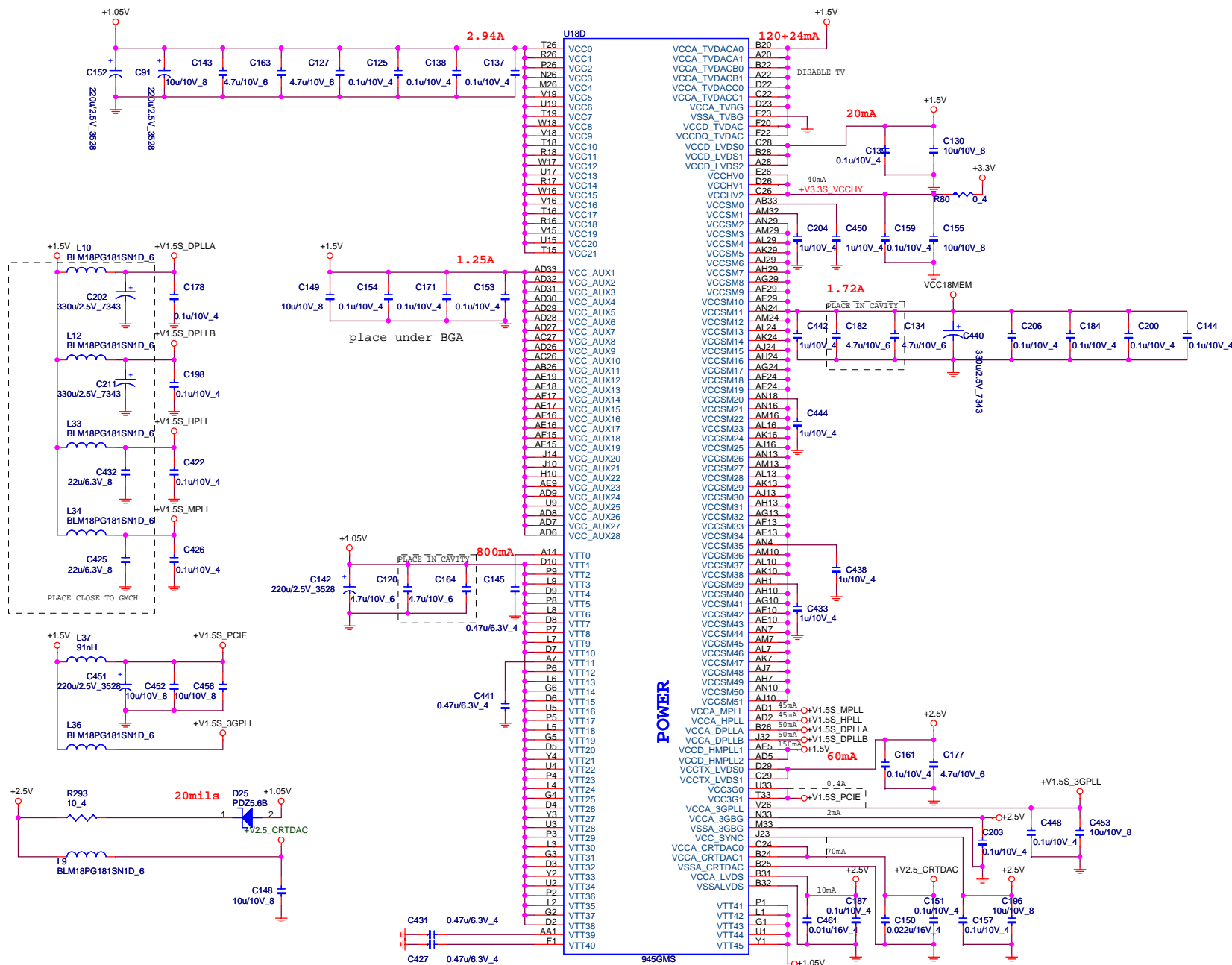
as close as possible to NB

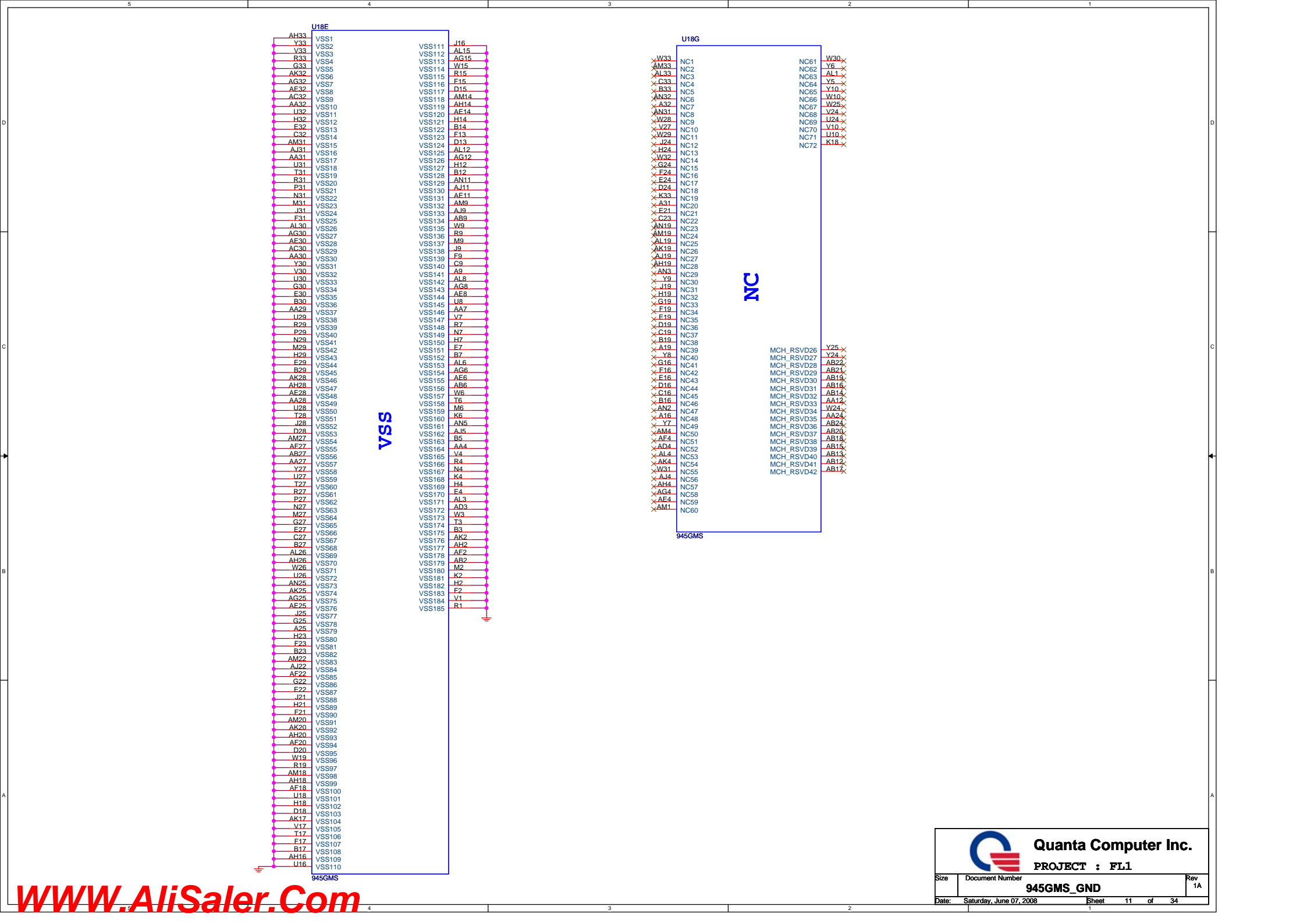
VGA_BLU -- R (150)

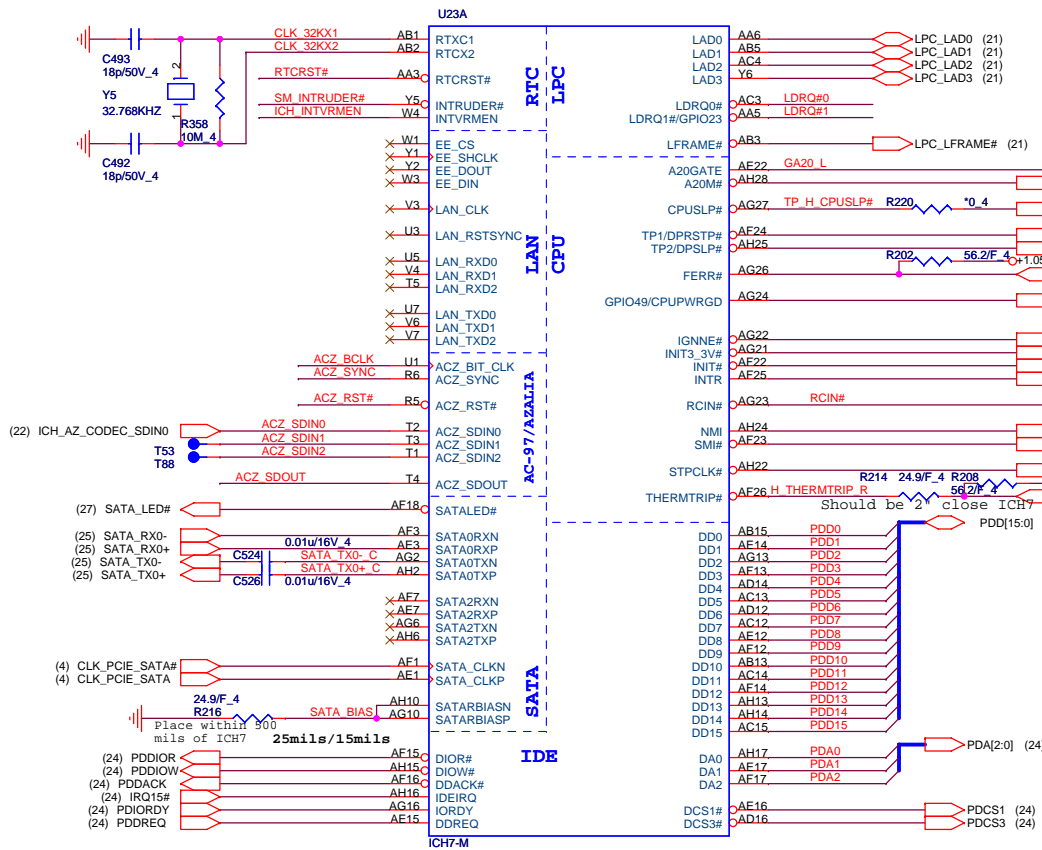
VGA_GRN -- R (150)

VGA_RED -- R (150)

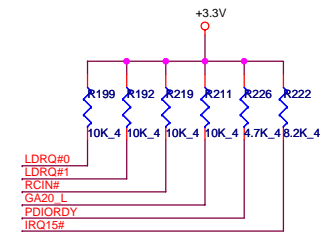




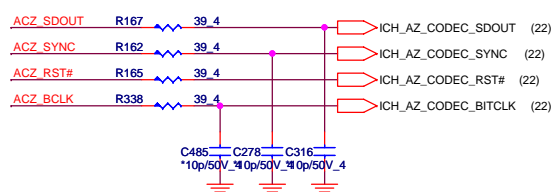
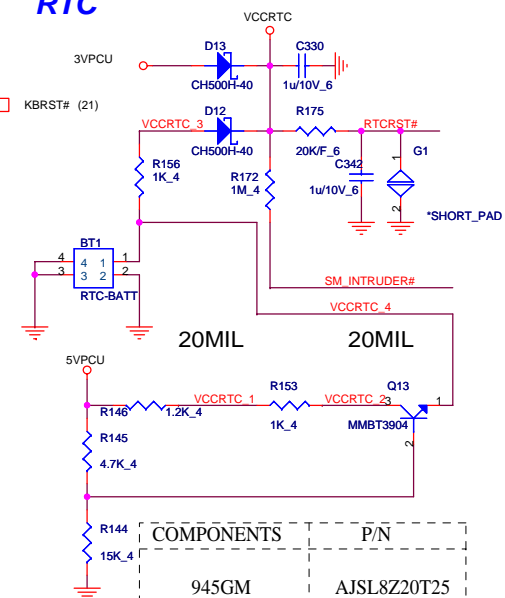




Pull-UP



RTC



	INTVRMEN
Enable (default)	1
Disable	0

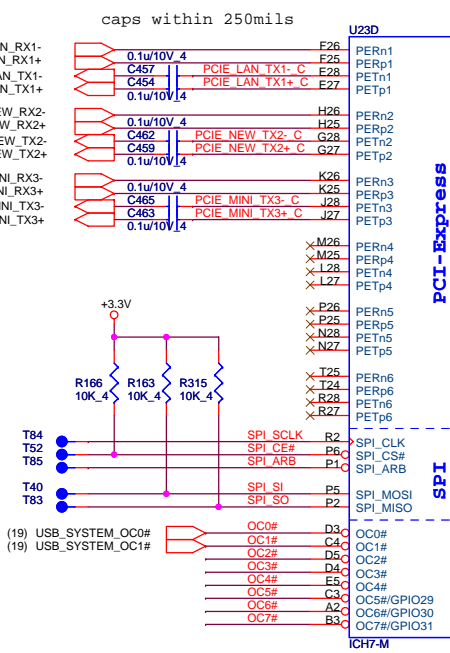
ICH7 internal VR enable strap



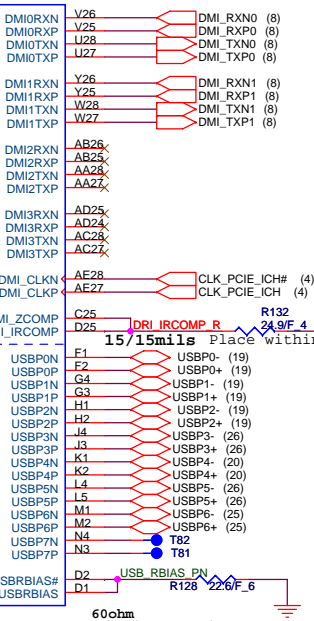
Please all series terms close to ICH7 except for SDIN input lines.

PCIE distribution

PCIE1	Enthernet Controller
PCIE2	New Card
PCIE3	MINI PCI-E Card
PCIE4	NC
PCIE5	NC
PCIE6	NC

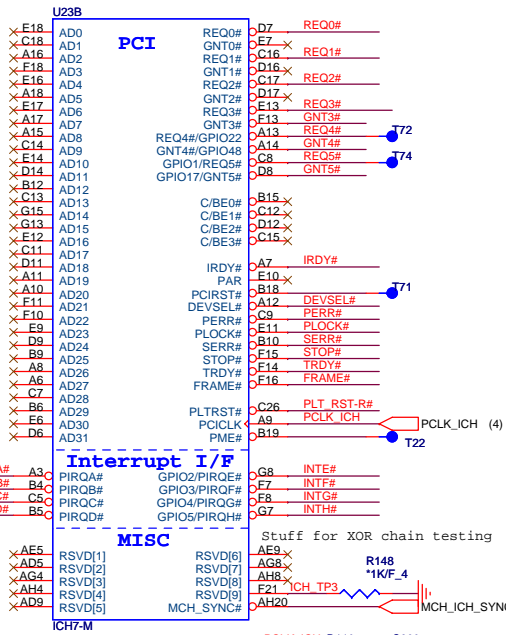
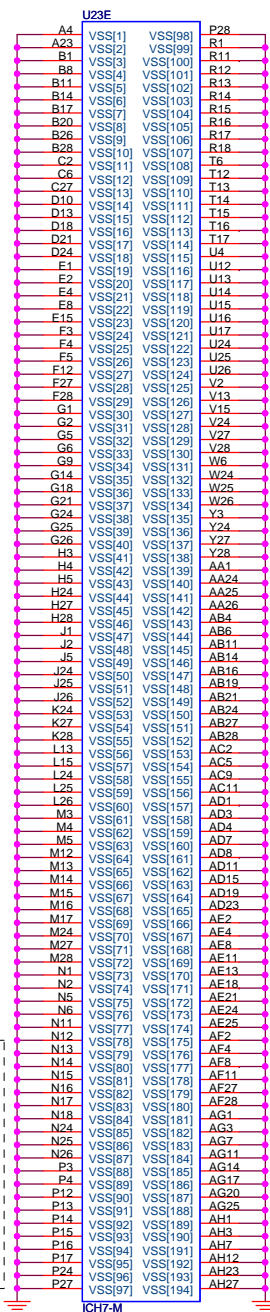


PCI-Express

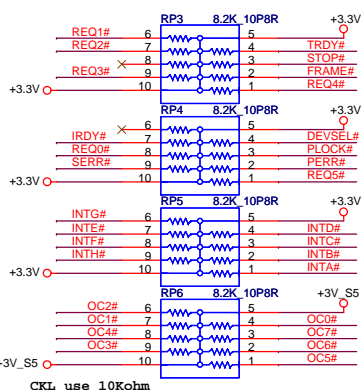


USB distribution

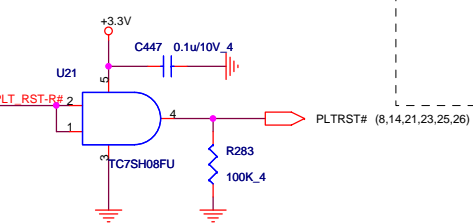
USB0	USB Port 1
USB1	USB Port 2
USB2	Camera
USB3	Bluetooth
USB4	Card Reader
USB5	MINI PCI-E CARD
USB6	New Card
USB7	Reserve



Pull-UP resistor



Platform Reset

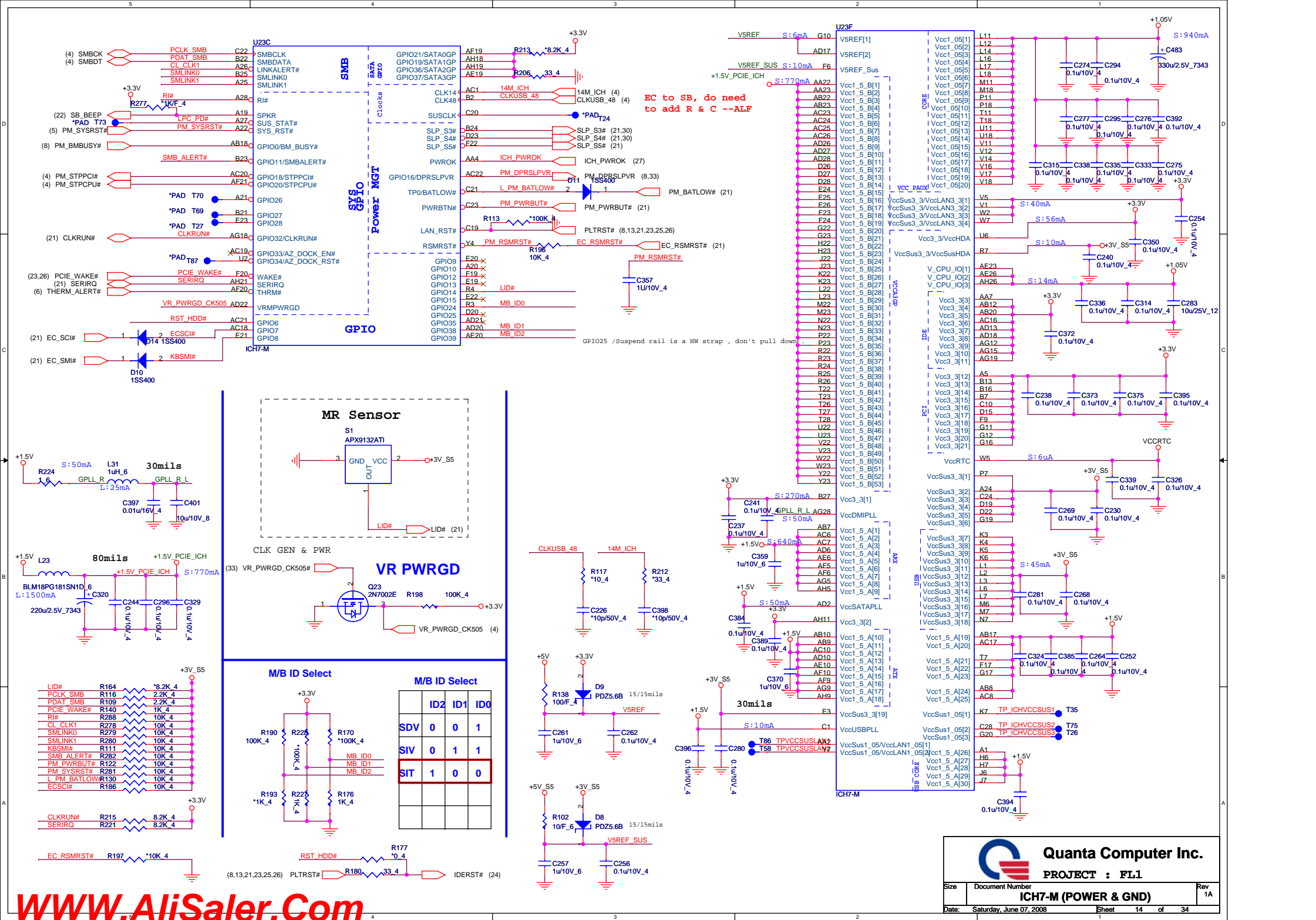


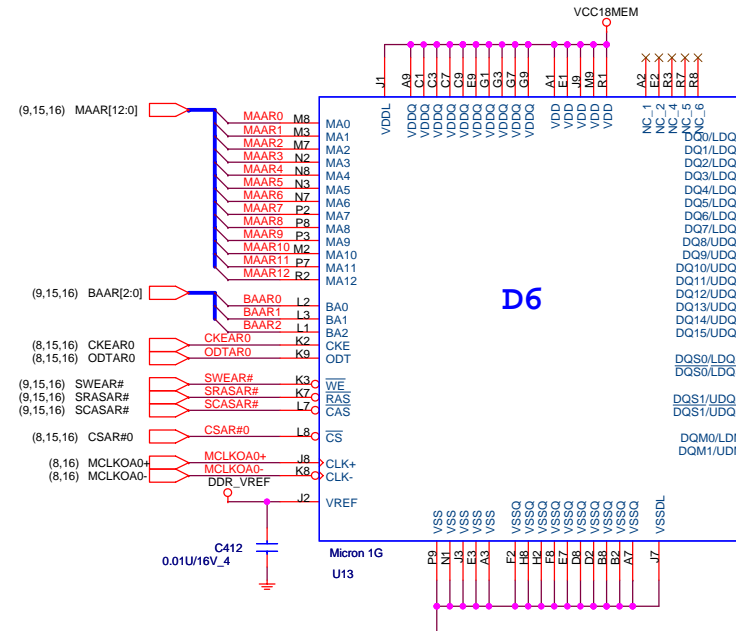
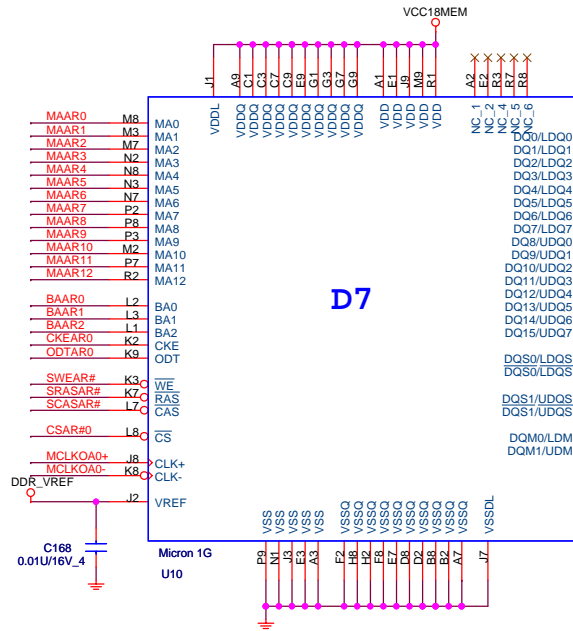
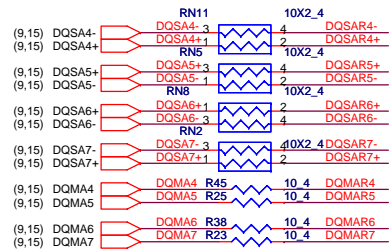
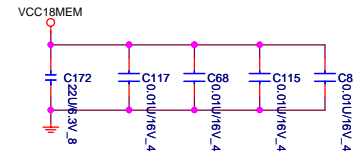
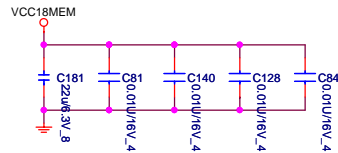
Boot BIOS select
01: SPI
10: PCI
11: LPC (Default)

GNT3# A16 SWAP OVERRIDE
Enable -- R375 Pop
Disable -- R375 De-Pop

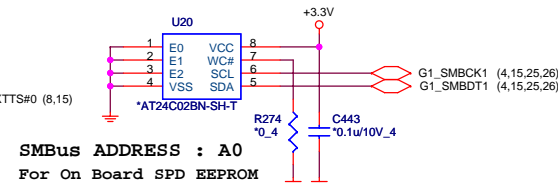
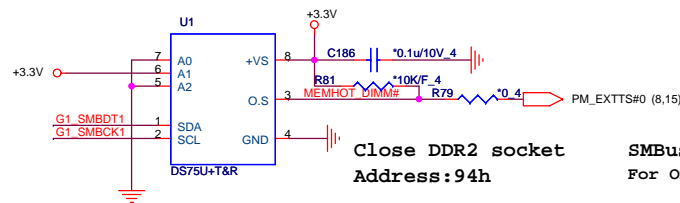
Quanta Computer Inc.
PROJECT : FL1

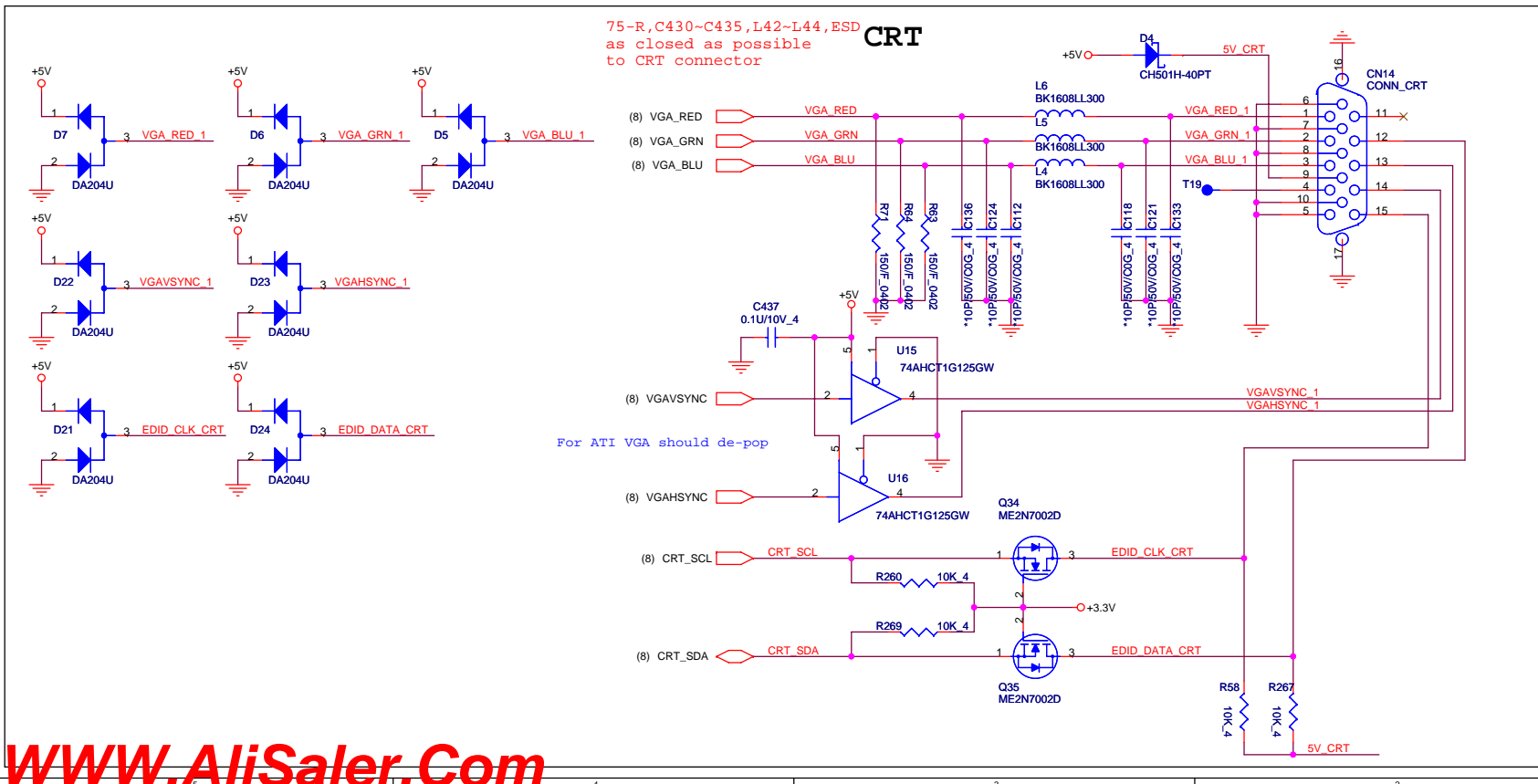
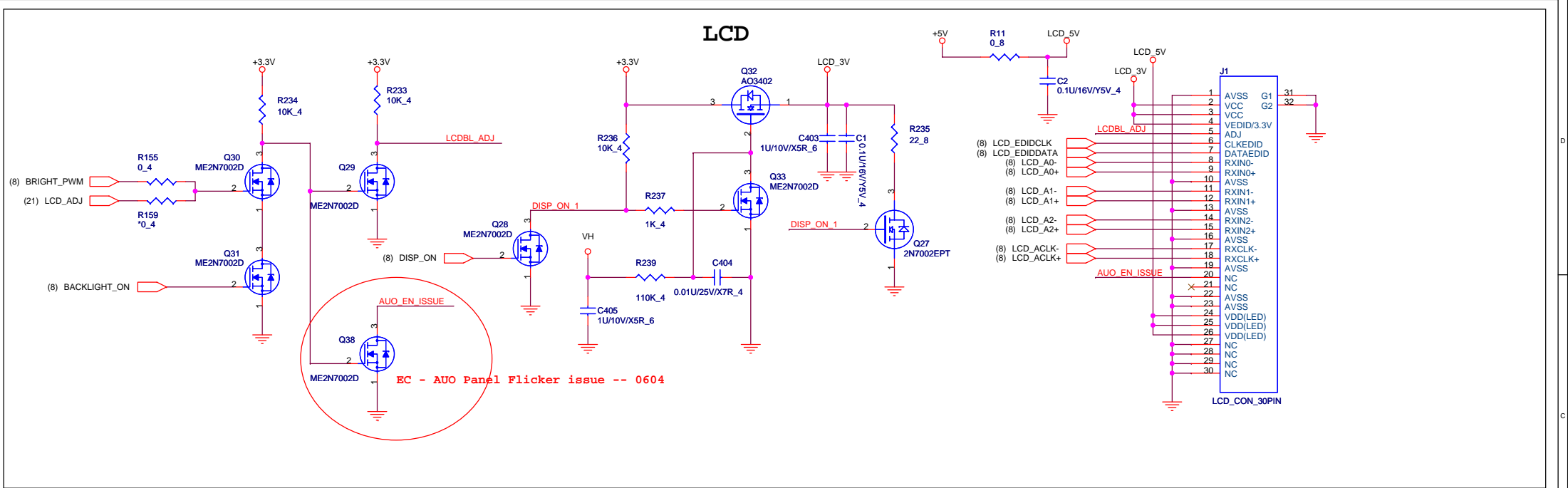
Size	Document Number	Rev
	ICH7-M (USB & DMI & PCIE & PCI)	1A
Date:	Saturday, June 07, 2008	Sheet 13 of 34



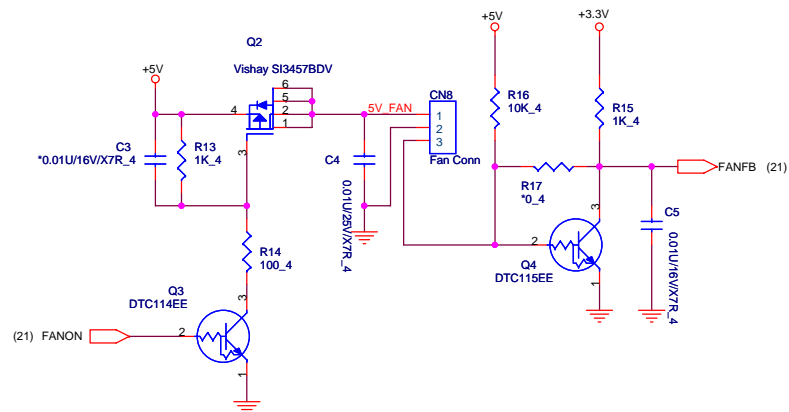


Reserve for On-Board RAM

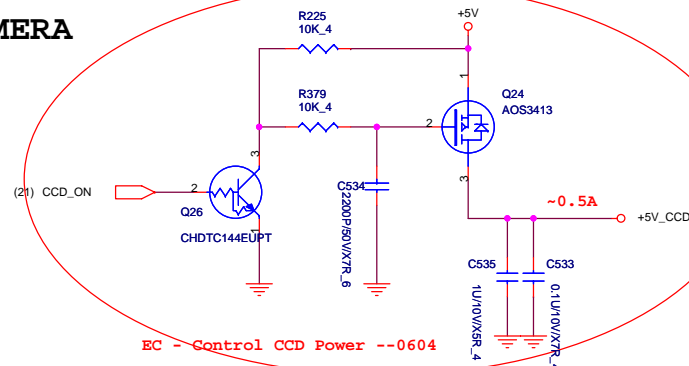




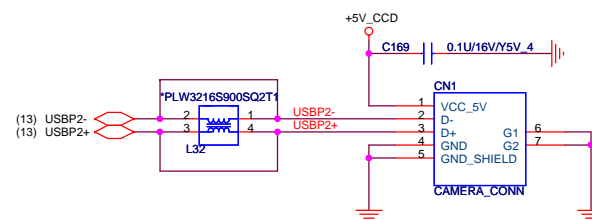
FAN



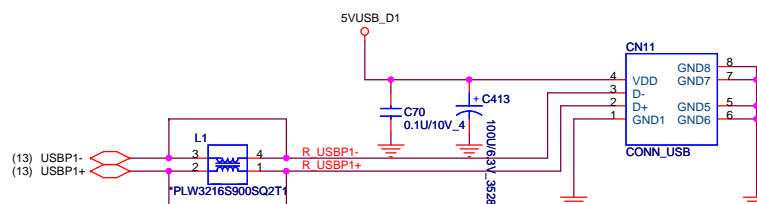
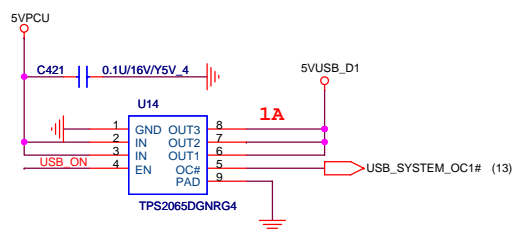
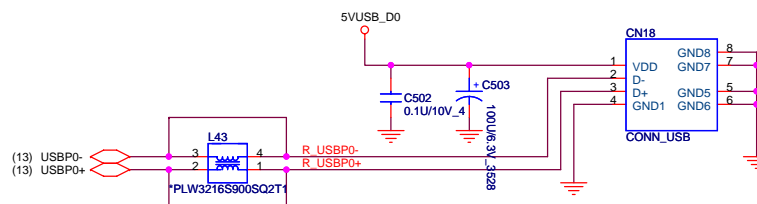
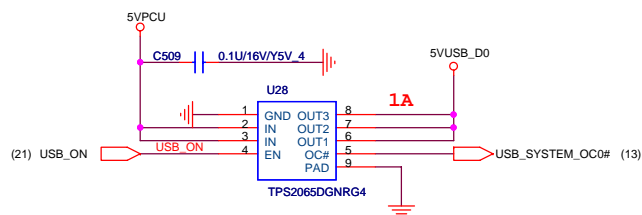
CAMERA



EC - Control CCD Power --0604



USB X 2 PORTS



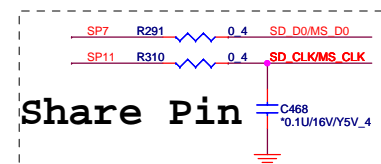
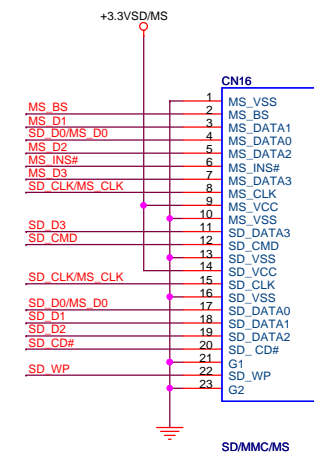
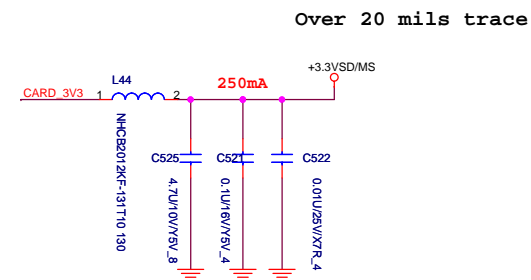
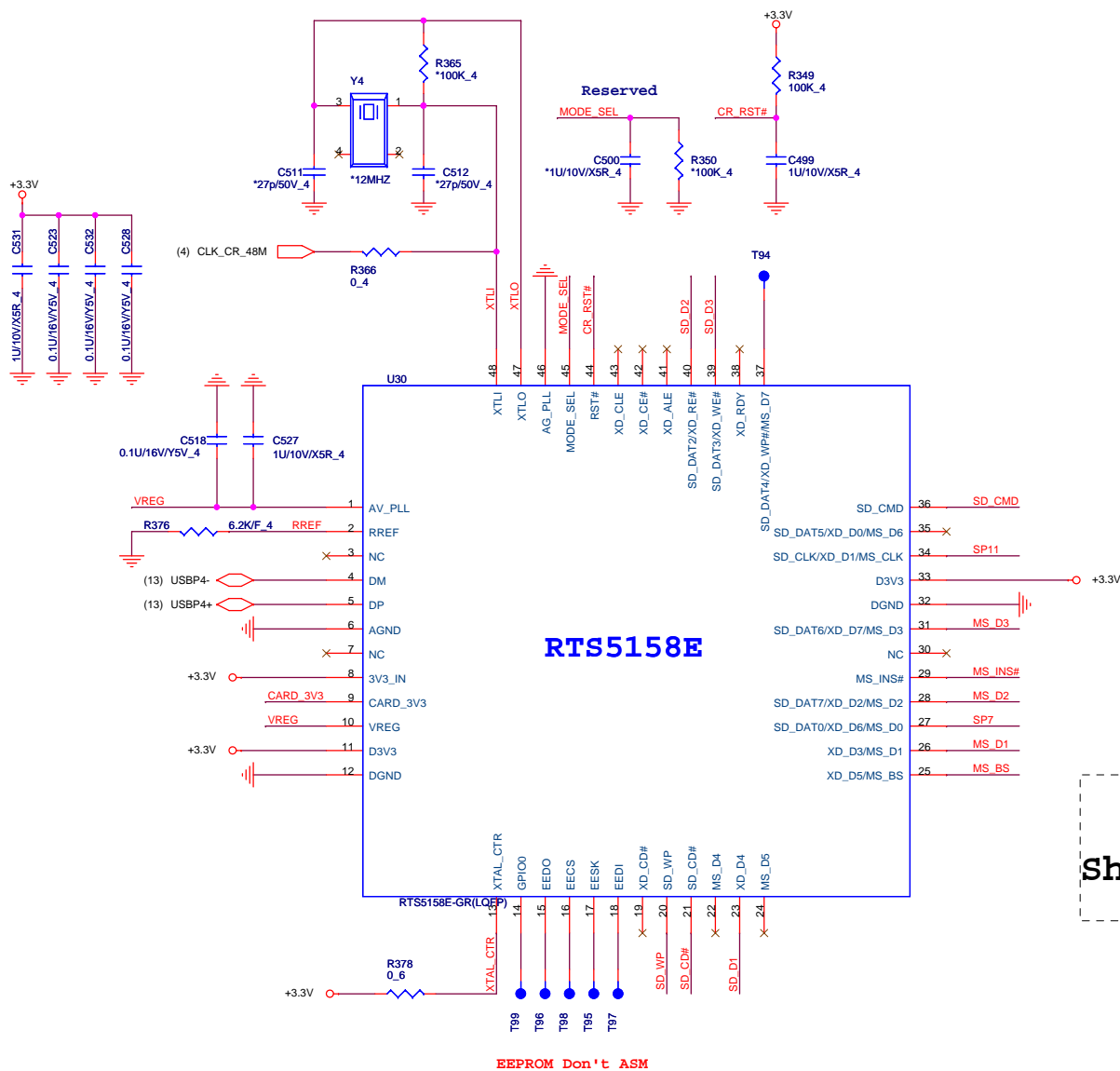
Quanta Computer Inc.

PROJECT : FL1

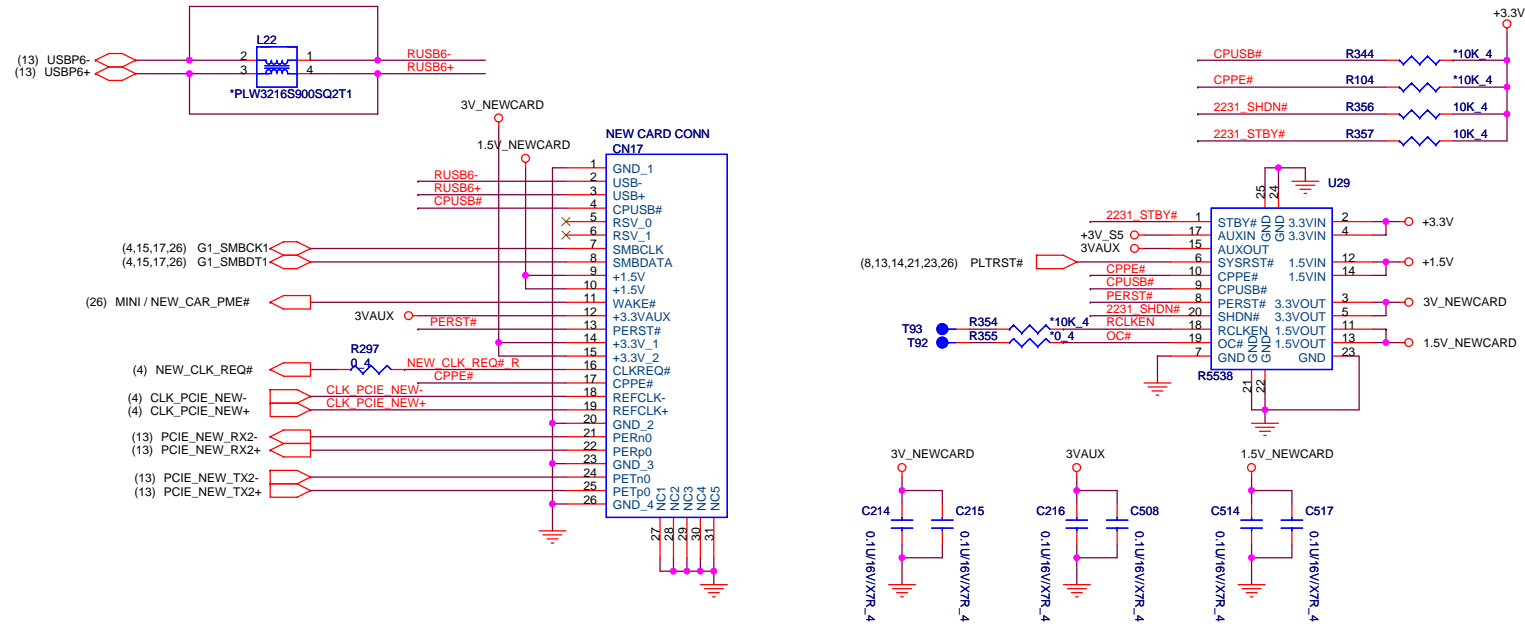
USB & FAN & CAMERA

Size	Document Number	Rev
1A	1A	1A

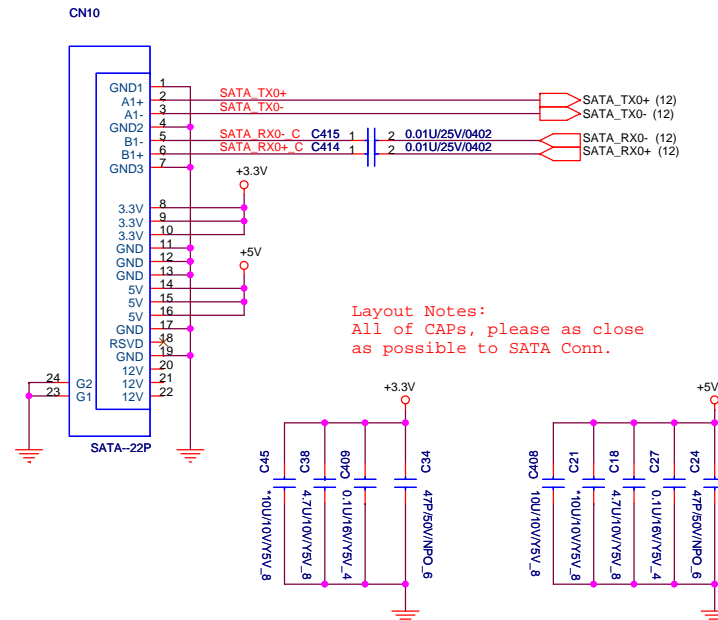
Date: Saturday, June 07, 2008 Sheet 19 of 34



NEW CARD



SATA CONNECTOR



Layout Notes:
All of CAPs, please as close
as possible to SATA Conn.

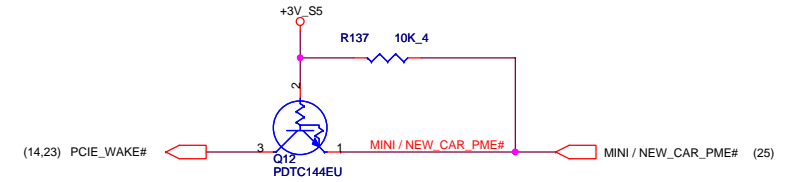
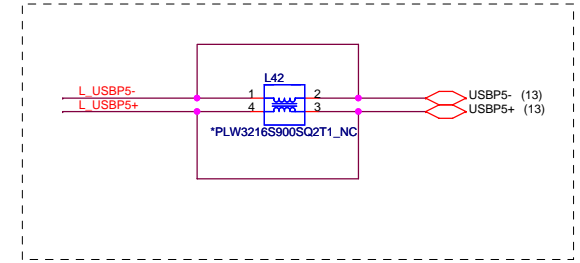
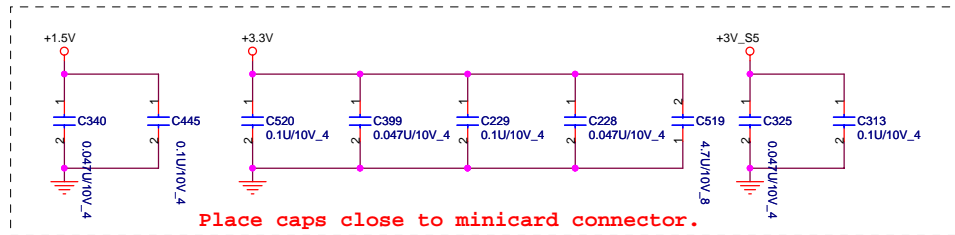
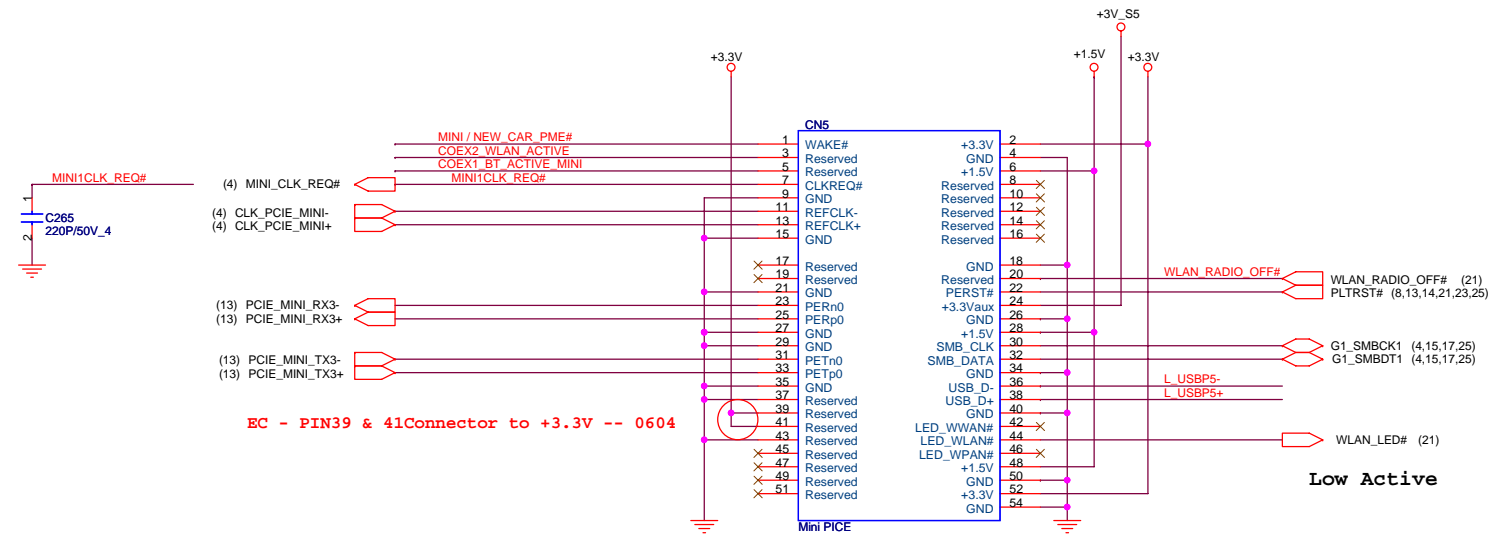


Quanta Computer Inc.

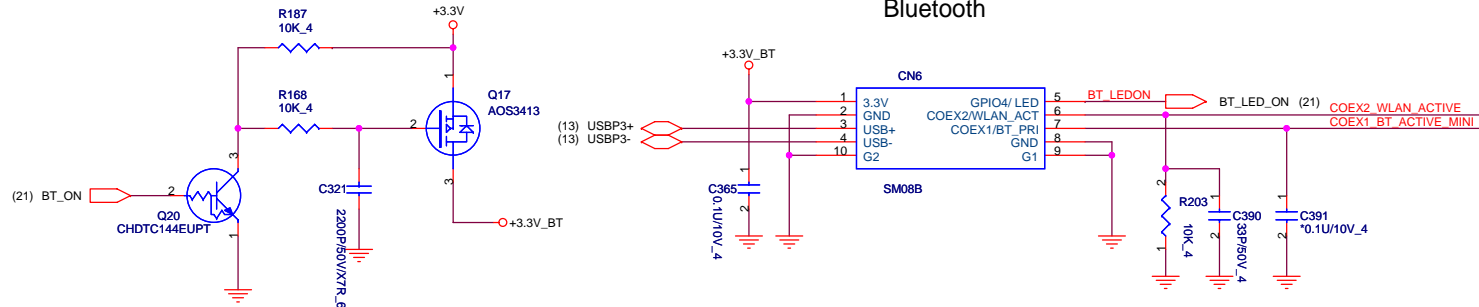
PROJECT : FL1

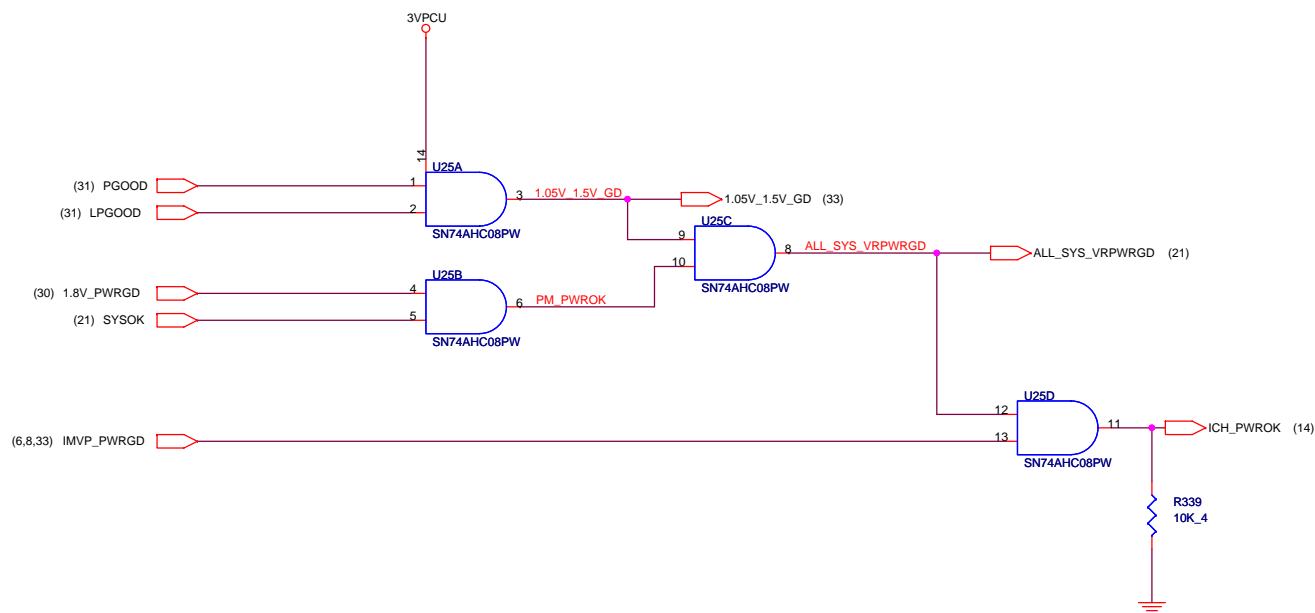
Size	Document Number	Rev
	NEW CARD / STAT	1A
Date:	Saturday, June 07, 2008	Sheet 25 of 34

MiniCard WLAN connector

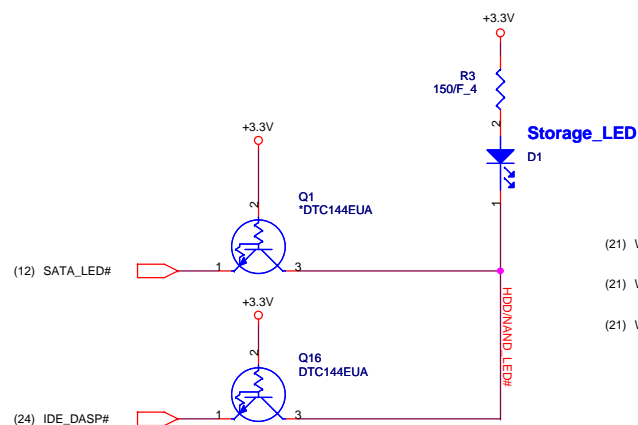


Bluetooth

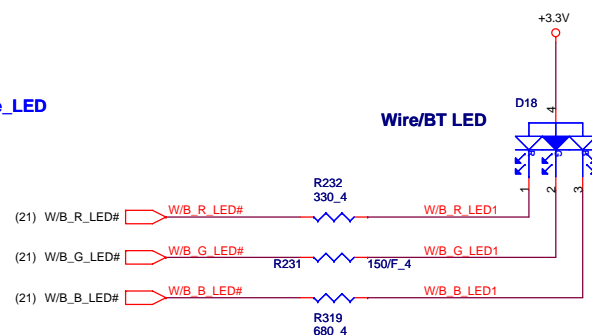




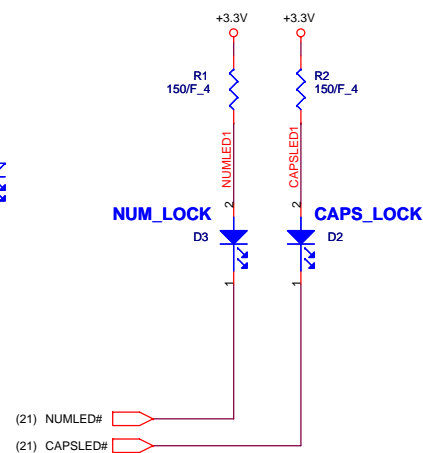
SATA/PATA LED



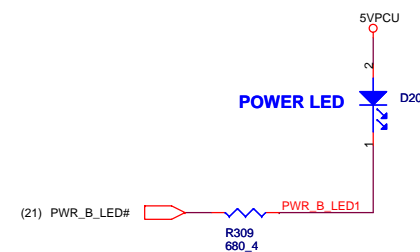
WLAN & BlueTooth LED



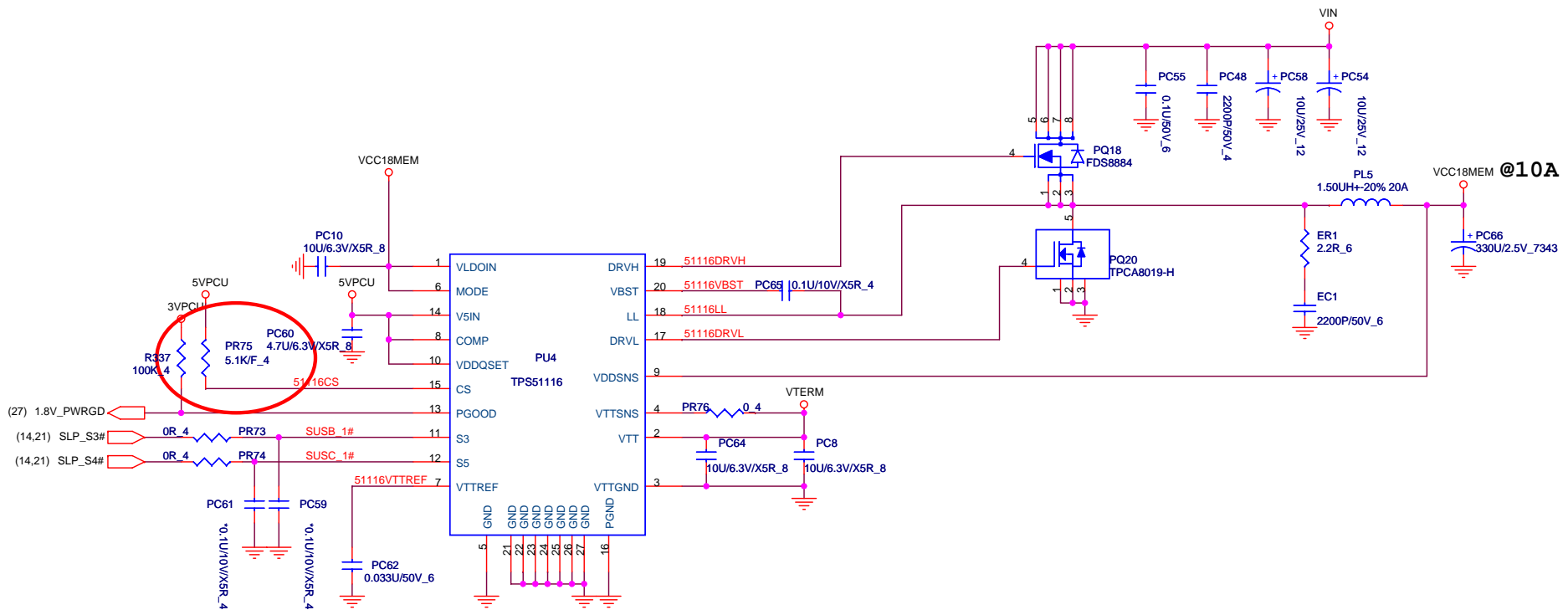
Number & CAPS Lock LED

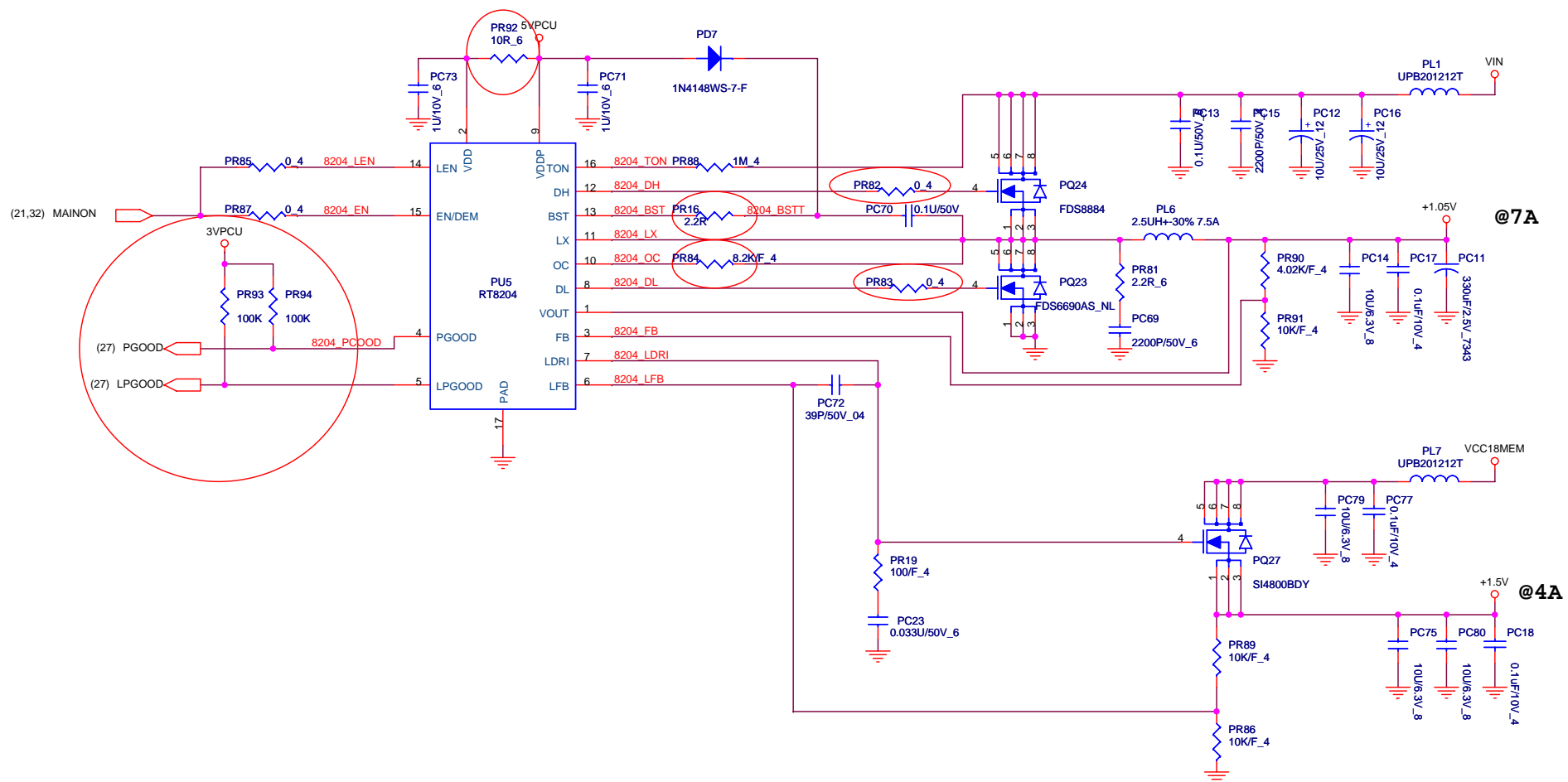


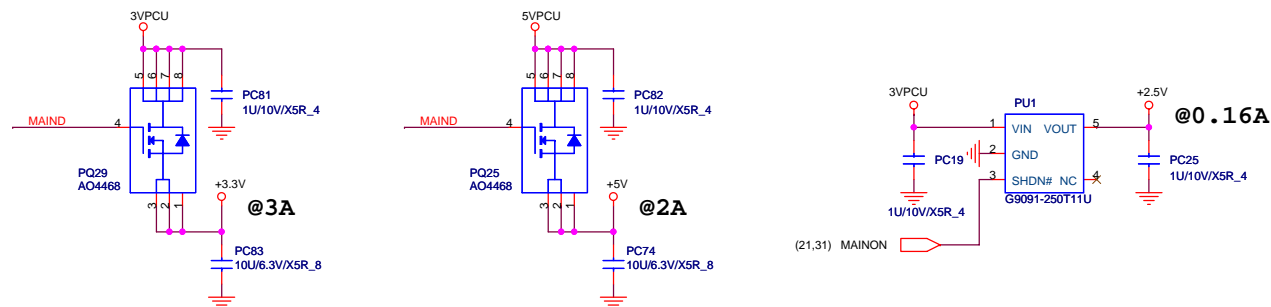
Power Button LED



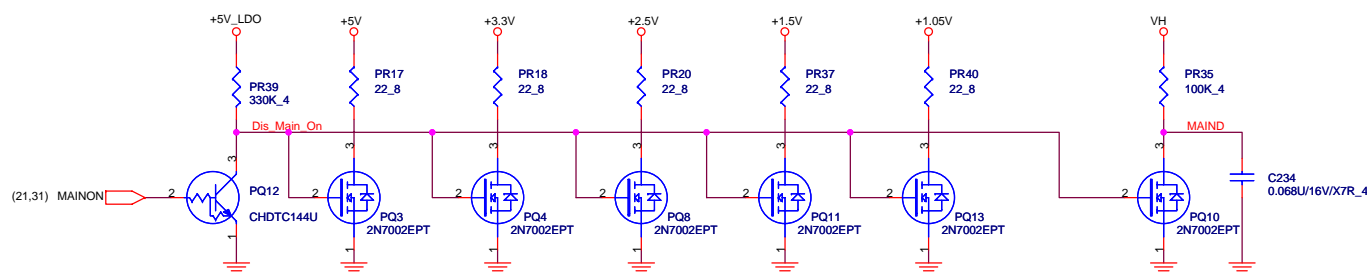
	Q1 & R5	Q16 & R6
SATA	ASM	No-ASM
PATA	No-ASM	ASM



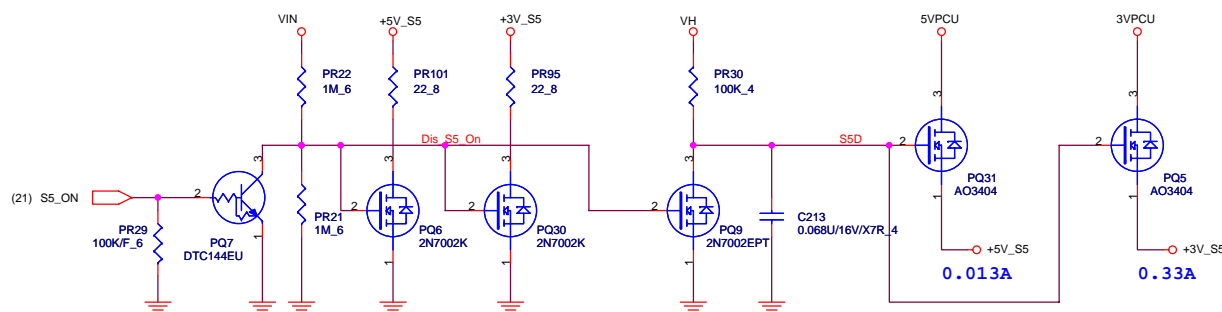




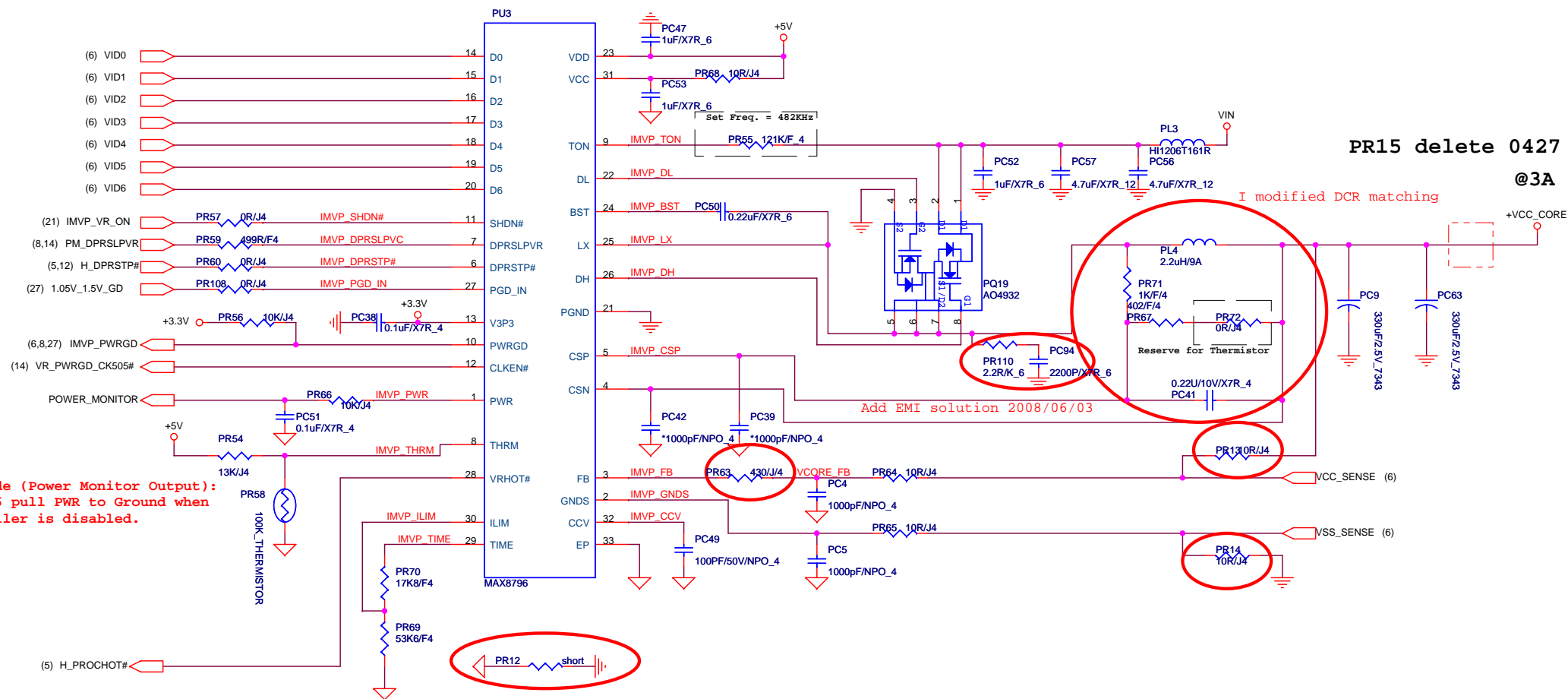
Discharge Circuit Controller by MAINON



Discharge Circuit Controller by S5_ON

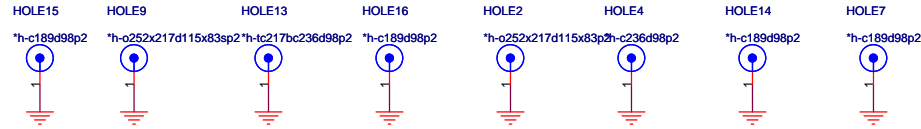


In Spec Side (Power Monitor Output):
The MAX8796 pull PWR to Ground when
the controller is disabled.

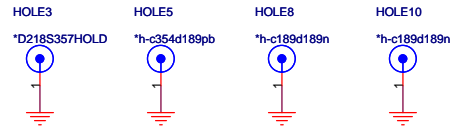


SCREW HOLE

正面沒有接地銅箔, 背面有



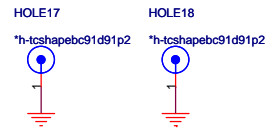
(d: 鑽孔的孔徑) 背面有露銅



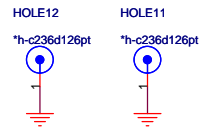
108HOLD



HDD -- HOLD



240HOLD



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PROJECT : FL1

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Date:	Saturday, June 07, 2008	Sheet 34 of 34

SCREW HOLD

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